## 32/16 CHANNEL 200MHz PC BASED LOGIC ANALYZER



## **GLA-1000 Series**



## **FEATURES**

- \* 32/16 Input Channel
- \* Time Analysis : Up to 200MHz Internal Sampling Rate
- \* State Analysis : Up to 100MHz External Input Clock
- \* Memory Depth : Up to 2Mbits/1Mbits (Half/Full Channels)
- \* Advanced Trigger : Count, Page, Time Delay and Clock Event Delay
- \* Data Compression : Up to 255 Times Data Capture
- \* Enable Qualifier : High/Low Level Enable and Enable Delay Function
- \* Signal Analyzer : I<sup>2</sup>C , RS-232C Protocol Analyzer and Signal Statistics
- \* File Export : Signal Data, Operation Setting and Display Image
- \* Manual Trigger Start Button
- \* High-Speed Communication : USB 2.0 (1.1 Compatible)
- \* USB Powered, Without External AC Power



GLA-1000 Series is a palm-size PC based logic analyzer with functionality matching that of a desktop device. The maximum 32 channels, 200MHz internal sampling rate, 1Mbits of data memory and advanced trigger settings give user higher accuracy and longer data analysis result. The unique features "Data-loss-less compression ", "Enable Qualifier " and "Serial Bus Protocol Analyzer " provide engineers & professionals with a mean, which can adequately handle today's digital system analysis. The whole captured data and setting of GLA-1000 Series could be saved as various types of files into PC for further data analysis, reporting, and other applications. The USB 2.0 communication between GLA-1000 Series and the PC guarantees the secured data transfer at the maximum speed while providing GLA-1000 Series with power source.

SPECIFICATIONS			
	GLA-1016	GLA-1032	GLA-1132
CHANNELS			
	16	32	32
MEMORY TOTAL/PER CHANNEL			
	4Mbits/256kbits	4Mbits/128kbits	32Mbits/1Mbits
INTERFACE			
USB 2.0 (1.1)			
OPERATING SYSTEM			
	Win98/98SE/Me/2000/XP		
TIMING ANALYSIS			
	Maximum 200MHz		
STATE ANALYSIS			
	0.001Hz ~ 100MHz		
TRIGGER			
Channel	16 (GLA-1016), 32 (GLA-1032/1132)		
Condition	Edge/Pattern		
Pre/Post trigger	0%~100%		
Level	1		
Threshold	$+6V \sim -6V$		
Accuracy	93mV Time delay, clock event delay		
Delay	Time delay, clock event delay		
Page	1 ~ 0333 Maximum 8191		
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Maximum Input Voltage	30V		
Data Skew	< 1.5ns		
ENABLE QUALIFIER			
Channel	16 (CLA.1016) 32 (CLA.1032/1132)		
Enable Condition	Don't care, Low, High		
Enable Delay	1~65535ms		
DATA			
Compression Channels	16 (GLA-1016), 24 (GLA-1032/1132)		
Compression Ratio	Maximum 255		
Signal Statistics	Postive/Negative, Full cycle within length condition		
Protocol Analyzer	1°C, RS-232C		
CURRENT CONSUMPTION			
Static Working	Maximum 200mA Maximum 400mA		
POWER DISSIPATION			
Static	Maximum 1W		
Working	Maximum 2W		
POWER			
Interface	USB		
Working voltage	4.5V ~ 5.5V		
DIMENSION & WEIGHT			
137/(V/ x 39/H) x 102/D) mm · Approx 340g			
13/ (w) x 3/ (1) x 102 (D) 11111, Appiox. 340g			



Level Configuration, Count/Page Delay, Time and Clock Event Delay

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